Form PTO-1449 (Modified)	Atty. Docket No. 1662-37500 (P00-3162)	Serial No. Not Yet Assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant Shubhendu S. MUKHERJEE	
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	Translation YES	NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

S G	AA	*AR-SMT: Microarchitectural Approach To Fault Tolerance In Microprocessors*, Eric Rotenberg, (8 p.).
S G	AB	*DIVA: A Dynamic Approach To Microprocessor Verification*, Todd M. Austin, Journal of Instruction-Level Parallelism 2 (2000) 1-6, Submitted 2/2000; published 5/2000 (26 p.).
S G	AC	*DIVA: A Reliable Substrate For Deep Submicron Microarchitecture Design*, Todd M. Austin, May/June 1999 (12 p.).

EXAMINER *[signature]*	DATE CONSIDERED 3/2/04

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Form PTO-1449 (Modified)					Atty. Docket No. 1662-37500 (P00-3162)		Serial No. 09/839,624

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
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